

**IN THE SPECIFICATION**

**Please amend the paragraph beginning on page 18, line 22 as follows:**

A third combination of the  $S_{IN}$  (0-X) and  $S_{OUT}$  (0-Y) signals selects a route between write strobe transceivers ~~[[141]]~~ 131 and input/output circuit 710. In some embodiments, the third combination of the  $S_{IN}$  (0-X) and  $S_{OUT}$  (0-Y) signals occurs during a low speed read operation in which data from memory array 102 are read to write strobe transceivers 131 via a route having elements connected by lines 165, 755A, and 155.